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**NEW QUESTION: 1** Refer to the exhibit. A Cisco Catalyst 6500 Series Switch experiences high CPU utilization. What can be the cause of this issue, and how can it be prevented?  
**A.** The hardware routing table is full. Redistribute from BGP into IGP.  
**B.** The software routing table is full. Redistribute from BGP into IGP.  
**C.** The software routing table is full. Reduce the number of routes in the routing table.  
**D.** The hardware routing table is full. Reduce the number of routes in the routing table.  
**Answer: D**  
**Explanation:** FIB TCAM Exception - If you try to install more routes than are possible into the FIB TCAM you will see the following error message in the logs:  
%CFIB-SP-7-CFIB\_EXCEPTION : FIB TCAM exception, Some entries will be software switched  
%CFIB-SP-7-CFIB\_EXCEPTION : FIB TCAM exception, Some entries will be software switched.  
%CFIB-SP-7-CFIB\_EXCEPTION : FIB TCAM exception, Some entries will be software switched.  
This error message is received when the amount of available space in the TCAM is exceeded. This results in high CPU. This is a FIB TCAM limitation. Once TCAM is full, a flag will be set and FIB TCAM exception is received. This stops from adding new routes to the TCAM. Therefore, everything will be software switched. The removal of routes does not help resume hardware switching. Once the TCAM enters the exception state, the system must be reloaded to get out of that state. You can view if you have hit a FIB TCAM exception with the following command:  
6500-2#sh mls cef exception status  
Current IPv4 FIB exception state = TRUE  
Current IPv6 FIB exception state = FALSE  
Current MPLS FIB exception state = FALSE  
When the exception state is TRUE, the FIB TCAM has hit an exception. The maximum routes that can be installed in TCAM is increased by the mls cef maximum-routes command.  
**Reference:**  
<https://supportforums.cisco.com/document/59926/troubleshooting-high-cpu-6500sup720>

**NEW QUESTION: 2** 特定の制限間の帯域幅を監視するようにIP SLAを設定する方法は？  
**A.** Frequency  
**B.** Timer  
**C.** Threshold  
**D.** Queue-limit  
**Answer: C**

**NEW QUESTION: 3** LDP penultimate hop pop-up mechanism (PHP), which allows the label to be ejected on the penultimate hop LSR. This can reduce the packet processing complexity of which options?  
**A.** Last hop and penultimate hop device  
**B.** Penultimate hop device  
**C.** Last hop device  
**D.** All MPLS network equipment  
**Answer: C**

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